

DEK STANDARD ENGINEERING INSTRUCTION

Title

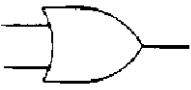
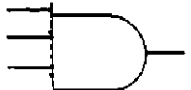
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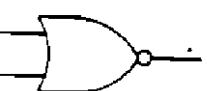
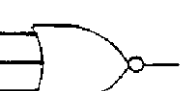
Electrical Component Symbols - Binary Logic Elements

DSE171

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Diagram A Gates

			
Integrated Circuit 2 I/P OR gate	Integrated Circuit 3 I/P OR gate	Integrated Circuit 2 I/P AND gate	Integrated Circuit 4 I/P AND gate
COMPONENT REF	COMPONENT REF	COMPONENT REF	COMPONENT REF
IC	IC	IC	IC

			
Integrated Circuit 2 I/P NOR gate	Integrated Circuit 3 I/P NOR gate	Integrated Circuit 2 I/P NAND gate	Integrated Circuit 4 I/P NAND gate
COMPONENT REF	COMPONENT REF	COMPONENT REF	COMPONENT REF
IC	IC	IC	IC

	
Integrated Circuit 2 I/P exclusive OR gate	Integrated Circuit Buffer
COMPONENT REF	COMPONENT REF
IC	IC

Issue	01	02	03	04	05	06	07	08
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Diagram B Invertors

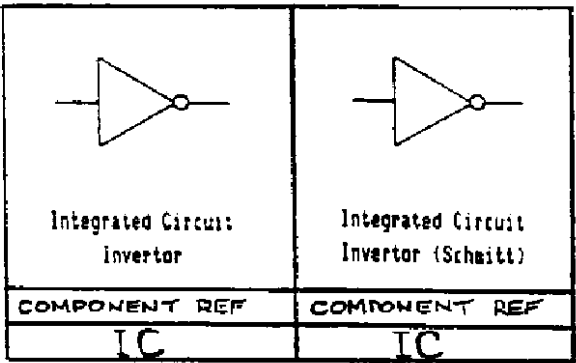


Diagram C Flip-Flops

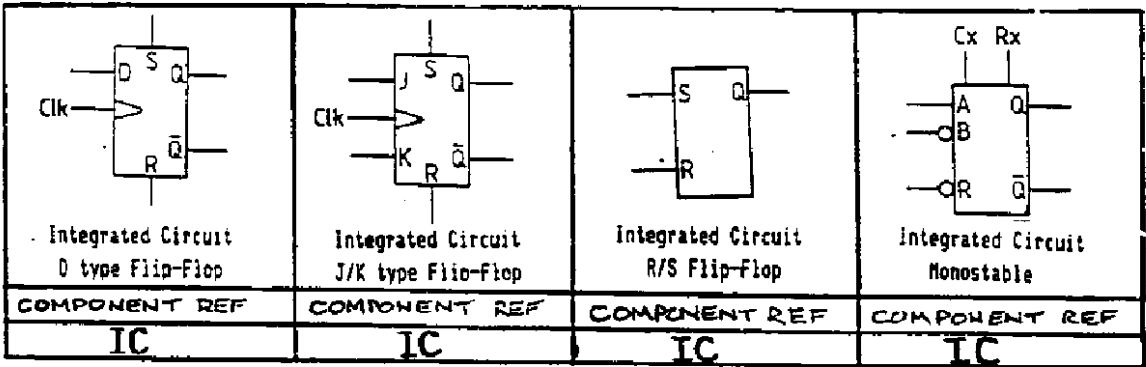
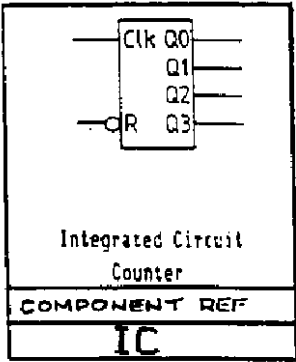


Diagram D Counters



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Diagram E Multiplexers and De-Multiplexers

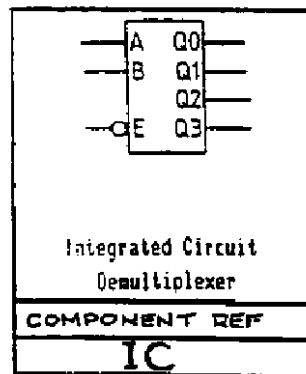


Diagram F Drivers

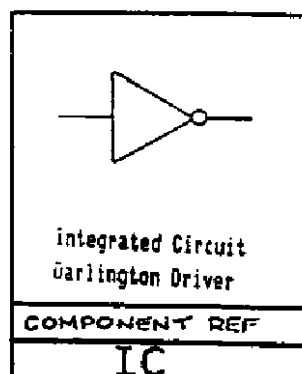
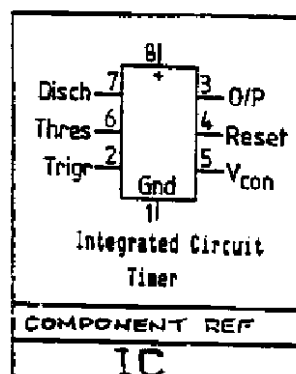


Diagram G Timers



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